COMMUNICATIONS

Initial bias dependency in dc drift of z-cut LiNbO₃ Optical intensity modulators

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1 Introduction

Estimation of a long-tenn dc drift behavior is essential for the application of LiNbO₃ optical intensity modulators to fiber communication systems. Therefore temperatureaccelerated tests are carried out using a feedback-biascontrol operation system, and the results are converted into a duration at ordinary device operation temperatures, e.g., 50°C. The activation energy E_a for the data conversion was reported to be about I eV for z-cut LiNbO₃ modulators.¹⁻³ In the feedback-bias-controlled operation, a certain dc voltage is applied to the ac-driven modulator sample as the initial dc bias, and this applied dc voltage is varied continuously to keep the state of the optical output modulation at the initial state.³⁻⁵ Furthermore, the initial bias voltage set to be the same for all samples is effective in simplifying the device qualification process. However, there is a possibility that the initial dc bias will change, sample by sample, because the bias voltage required to adjust the initial output modulation state depends not only on a device design but also on an initial temperature for the system operation.^{5,6} The output modulation states are strongly affected by mechanical fluctuation introduced into devices during the fabrication, and a complete equalization of them is difficult.⁶⁻¹⁰ To provide practical information on the longterm drift performance of z-cut LiNbO₃ modulators, we carried out experiments to show a relationship between the initial dc bias and the resulting dc drift. Because the z-cut LiNbO₃ modulators have been commonly used in both 2.5 and 10 Gbits/s systems, our results should be useful for a quahfication of LiNbO₃ devices. As a result, the applied voltage including the dc drift at a certain operation time was found to be proportional to the initial dc bias with an operation-time-dependent factor A(t) > I. Then, through a data fitting of temperature-accelerated test results at 120°C, the ultimate applied dc voltages after the 20 yrs of operation at 50°C were estimated to be from 10 to 1 5 V due to a change in the initial dc bias from 3.5 to 5.5 V.

2 Experiments and Results

To remove a difference in drift performance caused by device design, 8 pieces of Mach-Zehnder type 10 Gbits/s optical intensity modulators made from the same z-cut LiNbO₃ wafer were tested. The unbiased modulators were placed in an oven kept at 120°C, and after 4 h of storage the modulators reached this temperature and the measurements were started. The initial dc bias was applied individually to each of the ac-driven modulators, and the optical output signal ($=1.55 \mu$ m) was monitored. The initial dc bias was set at 3.5 V for one modulator, 4.5 V for two, 5.5 V for two, 6.5 V for two, and 7.5 V for one. The dc voltage applied to each of the modulators was controlled to maintain the optical output modulation at the initial dc bias was state (control frequency = I kHz.)

Figure I shows measured results as a relationship between applied dc voltage and device operation time at 120°C with a linear time axis [Fig. I (a)] and a logarithmic time axis [Fig. 1(b)]. Using E_a = I eV reported for dc drift in z-cut LiNbO₃ modulators,¹ the 120 hours at 120°C can be converted to the 8.2 years at 50°C for instance. Because the limit of the dc driver used was ±10 V, samples started from higher initial dc biases got to be in an uncontrollable state within a day.

3 Discussion and Long-Term Drift Estimation

Figure 2 reveals the correlation between the applied dc voltage and the initial dc bias obtained after I h (black circles), 5 h (white circles), and 50 h (black triangles) of operation. Similar results were obtained for other data from different operation times, and the following relationship was found:

$$V_{\text{appl}} = A(t) V_{\text{initial}^{\circ}}$$
(1)

Here, V_{initial} rs the initial dc bias, V_{appl} is the applied dc voltage at time *t* including the initial dc bias, and A (t) is a proportional factor as a function of the operation time *t*. The relationship denoted by Eq. (1) is consistent with a fact that unbiased (dc bias =0 V) modulators do not dnft at a constant temperature.^{6,11} Furthermore, the proportional factor A (*t*) was found to be larger than I and increase with

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Fig. 1 Feedback-bias-controlled operation results for z-cut LiNbO₃ modulators at 120°C with various initial dc biases from 3.5 to 7.5 V with (a) a linear time axis and (b) a logarithmic time axis. The vertical axes denote the applied dc voltage including the initial dc bias.



Fig. 2 Relationship between an applied dc voltage and an initial dc bias plotted using the data of the lst, 5th, and 50th hour in Fig. 1. Solid lines are fifting results obtained by simple proportional equations, and the dashed line denotes a relationship with a proportional factor= 1.



Fig. 3 Operation time dependency of the proportional factor A(t) obtained by plots, as show in Fig. 2, with (a) a linear time axis and (b) a logarithmic time axis.

time *t*. This result indicates that the applied dc voltage expands more largely with the factor A(t) for devices operated with higher initial dc biases. In other words, the dc drift rate of LiNbO₃ modulators depends on the magnitude of the initial dc bias, as we previously suggested. ^{12,13}

The operation time dependency of the factor A(t) is plotted in Fig. 3(a), showing a logarithmic-like change against the time axis at 120°C. Figure 3(b) reveals a numerical fitting result of the data by an expression of the third order for the logarithmic time axis, In(t). Although a physical meaning of the expression is not currently known, the derived expression can be used to estimate the initial dc bias dependency of the long-tenn drift.

Figure 4(a) denotes the dnft curves calculated using the preceding expressions, in which measured data of Fig. 1 (a) are also plotted. Similar drift curves calculated for the operations at 85 and 50°C are shown in Figs. 4(b) and 4(c), respectively. In the first step of the calculation, an expression similar to that in Fig. 3(b) for A(t) was derived using a new time axis at 85°C (50°C), which was converted from 120°C data using E_a = I eV. Then, the A(t) values calculated for certain times at 85 and 50°C were substituted for Eq. (1), and the applied dc voltages were plotted as the function of time and initial dc bias.

The results of Fig. 3(c) indicate that if the performance of the dc dnver is limited by a 15-V maximum, for in-



Fig. 4 Initial bias dependency of dc drift of z-cut LiNb03 modulators estimated for operations at (a) 120°C, (b) 85°C, and (c) 50°C. The vertical axis denotes applied dc voltages including both the initial dc bias and the drift.

stance, the initial dc bias applied to LiNbO₃ modulators must be set to less than 5.5 V to ensure their 20-yr lifetime at 50°C. In other words, the total fluctuation of the modulator output due to thermal dnft, modulation half-wave voltage ($V\pi$), initial operating point, etc. must be suppressed below 5.5 V.

4 Conclusion

The initial dc bias dependency of the long-term dc dnft in z-cut LiNbO₃ optical intensity modulators was estimated from temperature accelerating test results at 120°C. The applied dc voltages after 20 yr of operation at 50°C were estimated to increase from 10 to 15 V due to a change in the initial dc bias from 3.5 to 5.5 V.

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